

PATENTS

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Kim et al.

Serial No.: **10/706,187**Art Unit: **2661**Filed: **November 12, 2003**Confirmation No.: **6130**For: **High-Speed Analog-to-Digital  
Conversion With Improved  
Robustness to Timing  
Uncertainty****INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
 P. O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

The citation of information on the attached Form PTO-1449, "List of Art Cited by Applicant" is made pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98. A copy of each cited item is enclosed.

The citation of this information does not constitute an admission of priority or that any cited item is available as a reference, or a waiver of any right the applicant may have under applicable statutes, Rules of Practice in patent cases, or otherwise.

Respectfully submitted,

  
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 K&S Docket: 07982.105019 US  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on May 18, 2004.

  
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(Use several sheets if necessary)

ATTY. DOCKET NO. 07982.105019	SERIAL NO. 10/706,187	FILING DATE November 12, 2003
APPLICANT Kim et al.		GROUP 2661

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**MAY 20 2004**  
**SEARCH & TRADEMARK OFFICE**

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA	5,181,136	1/19/1993	Kavehrad et al.			9/20/1990
AB	5,625,722	4/29/1997	Froberg et al.			12/21/1994
AC	6,002,717	12/14/1999	Gaudet, Brian			5/28/1997
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AF	6,501,792 B2	12/31/2002	Webster, Stephen Paul			9/6/2001
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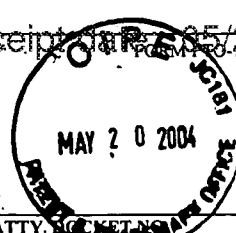
**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	NAME	TRANSLATION	
					YES	NO
AL	WO 02/067521 A1	8/29/2002	PCT	Vrazel et al.		
AM	WO 02/091600 A2	11/14/2002	PCT	Schmukler et al.		
AN	WO 03/077423 A2	9/18/2003	PCT	Hietala et al.		
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AQ						
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AS						

EXAMINER	/Sam Rizk/	DATE CONSIDERED	/S.R./ 10/13/2009
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APPLICANT Kim et al.		GROUP 2661

**OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)**

BA	Choi et al.; <i>A 0.18-μm CMOS 3.5-Gb/s Continuous-Time Adaptive Cable Equalizer Using Enhanced Low-Frequency Gain Control Method</i> ; IEEE Journal of Solid-State Circuits; March 2004; Vol. 39, No. 3; pp. 419-425
BB	Paul, et al.; <i>3 Gbit/s Optically Preamplified Direct Detection DPSK Receiver With 116 photon/bit Sensitivity</i> ; Electronics Letters; Vol. 29, No. 7; April 1, 1993; pp. 614-615
BC	Penninckx et al.; <i>Optical Differential Phase Shift Keying (DPSK) Direct Detection Considered as a Duobinary Signal</i> ; Proc. 27 <sup>th</sup> Eur. Conf. on Opt. Comm. (ECOC'01 – Amsterdam); Vol. 3; September 30 to October 4, 2001; pp. 456-457
BD	Rohde et al.; <i>Robustness of DPSK Direct Detection Transmission Format in Standard Fibre WDM Systems</i> ; Electronics Letters; Vol. 36, No. 17; August 17, 2000; pp. 1483-1484
BE	Shirasaki et al.; <i>Fibre Transmission Properties of Optical Pulses Produced Through Direct Phase Modulation of DFB Laser Diode</i> ; Electronics Letters; Vol. 24, No. 8; April 14, 1988; pp. 486-488
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